

Polyhedral Compilation without Polyhedra

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Outline

1 Polyhedral Model

- Introduction
- Representation

2 Polyhedral Transformation

- Schedules
- AST Generation

3 Dependences

- Schedule Validity
- Dependences
- Structures

8 Tools

4 Dataflow

- Parallelism
- Dataflow
- Approximate Dataflow
- Run-time dependent Dataflow
- Reductions

5 Aliasing

6 Counting

- Cardinality
- Bounds
- Weighted Counting
- Dynamic Memory Requirement

7 Transitive Closures

Part I

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Polyhedral Compilation

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Analyzing and/or transforming loop programs using the *polyhedral model*

Polyhedral Model

Abstract representation of a loop program

- instance based
 - ⇒ statement *instances*
 - ⇒ array *elements*
- compact representation based on polyhedra or similar objects
 - ⇒ integer points in unions of parametric polyhedra
 - ⇒ Presburger sets and relations
- parametric
 - ⇒ description may depend on symbolic constants

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Note: naming is “historical”

- polyhedral compilation does not require polyhedra (e.g., $\omega(+)$)
- other approaches also use polyhedra (e.g., abstract interpretation)

Polyhedral Model

Main constituents of program representation

- **Instance Set**
 - ⇒ the set of all statement instances
- **Access Relations**
 - ⇒ the array elements accessed by a statement instance
- **Dependences**
 - ⇒ the statement instances that depend on a statement instance
- **Schedule**
 - ⇒ the relative execution order of statement instances

Polyhedral Model Requirements

Requirements for **basic** polyhedral model: SANA input

- Static control
 - ⇒ control does not depend on input data
- Affine
 - ⇒ all relevant expressions are (quasi-)affine
- No Aliasing
 - ⇒ essentially no pointer manipulations

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Note:

- polyhedral model may be *approximation* of input that does not strictly satisfy all requirements
- many *extensions* are available
 - a small selection of these extensions will be discussed in this tutorial

Illustrative Example

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R:  h(A[2]);  
    for (int i = 0; i < 2; ++i)  
      for (int j = 0; j < 2; ++j)  
S:      A[i + j] = f(i, j);  
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$I = \{ R(); S(0,0); S(0,1); S(1,0); S(1,1); T(0); T(1) \}$

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- **Schedule** (relative execution order)

$$R(), S(0,0), S(0,1), S(1,0), S(1,1), T(0), T(1)$$

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- Access Functions

$$W = \{ R(); S(i,j) : 0 \leq i < 2 \wedge 0 \leq j < 2; T(k) : 0 \leq k < 2; \}$$

• “read off”, or

$$R = \{ R(); S(0,0) \rightarrow A(0); S(0,1) \rightarrow A(1); S(1,0) \rightarrow A(1); S(1,1) \rightarrow A(2); T(0) \rightarrow A(0); T(1) \rightarrow A(0) \}$$

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Parametric Example: Matrix Multiplication

```

for (int i = 0; i < M; i++)
  for (int j = 0; j < N; j++) {
S1:    C[i][j] = 0;
        for (int k = 0; k < K; k++)
S2:      C[i][j] = C[i][j] + A[i][k] * B[k][j];
  }

```

- **Instance Set** (set of statement instances)

$$\begin{aligned}
 &\{ \text{S1}(i,j) : 0 \leq i < M \wedge 0 \leq j < N; \\
 &\quad \text{S2}(i,j,k) : 0 \leq i < M \wedge 0 \leq j < N \wedge 0 \leq k < K \}
 \end{aligned}$$

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- **Schedule** (relative execution order)

$$\text{S1}(0,0), \text{S2}(0,0,0), \text{S2}(0,0,1), \dots, \text{S1}(0,1), \text{S2}(0,1,0), \text{S2}(0,1,1), \dots,$$

Named Presburger Sets and Relations

[20]

Examples

$$\{ R(); S(i, j) : 0 \leq i < 2 \wedge 0 \leq j < 2; T(k) : 0 \leq k < 2 \}$$

$$\{ R() \rightarrow A(2); T(k) \rightarrow A(0) : 0 \leq k < 2; T(k) \rightarrow A(k) : 0 \leq k < 2 \}$$

General form

- Sets

$$\{ S_1(\mathbf{i}) : f_1(\mathbf{i}); S_2(\mathbf{i}) : f_2(\mathbf{i}); \dots \},$$

with f_k Presburger formulas

\Rightarrow set of elements of the form $S_1(\mathbf{i})$, one for each \mathbf{i} satisfying $f_1(\mathbf{i})$, ...

- Binary relations

$$\{ S_1(\mathbf{i}) \rightarrow T_1(\mathbf{j}) : f_1(\mathbf{i}, \mathbf{j}); S_2(\mathbf{i}) \rightarrow T_2(\mathbf{j}) : f_2(\mathbf{i}, \mathbf{j}); \dots \}$$

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Note: despite “ \rightarrow ”, not necessarily (single valued) functions

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Note: may depend on interpretation of symbolic constants

$$\{ S(i) : 0 \leq i \leq n() \}$$

is equal to

$$\begin{cases} \emptyset & \text{if } n < 0 \\ \{ S(0) \} & \text{if } n = 0 \\ \{ S(0); S(1) \} & \text{if } n = 1 \\ \{ S(0); S(1); S(2) \} & \text{if } n = 2 \\ \dots & \end{cases}$$

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- Presburger Formula
 - ▶ true
 - ▶ equality on terms (=)
 - ▶ less than or equal on terms (\leq)
 - ▶ logical connectives (\wedge, \vee, \neg)
 - ▶ quantification (\exists, \forall)

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- $a, b \oplus c$ is equal to $a \oplus c \wedge b \oplus c$ with $\oplus \in \{ \leq, <, \geq, >, = \}$
Example: $\{ S(i, j) : i, j \geq 0 \}$ is equal to $\{ S(i, j) : i \geq 0 \wedge j \geq 0 \}$

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- $a \oplus_1 b \oplus_2 c$ is equal to $a \oplus_1 b \wedge b \oplus_2 c$ with $\{ \oplus_1, \oplus_2 \} \subset \{ \leq, <, \geq, >, = \}$
 Example: $\{ S(i) : 0 \leq i \leq 10 \}$ is equal to $\{ S(i) : 0 \leq i \wedge i \leq 10 \}$

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- $a \bmod n$ is equal to $a - n \lfloor a/n \rfloor$

Spaces

Recall general form

- Sets

$$\{ S_1(\mathbf{i}) : f_1(\mathbf{i}); S_2(\mathbf{i}) : f_2(\mathbf{i}); \dots \},$$

- Binary relations

$$\{ S_1(\mathbf{i}) \rightarrow T_1(\mathbf{j}) : f_1(\mathbf{i}, \mathbf{j}); S_2(\mathbf{i}) \rightarrow T_2(\mathbf{j}) : f_2(\mathbf{i}, \mathbf{j}); \dots \}$$

The **identifier** (e.g., S_1 , S_2 , T_1 , T_2), together with the **dimension**, i.e., number of elements in subsequent tuple (e.g., \mathbf{i} , \mathbf{j}), will be called a **space**

When we say $S_2(\mathbf{i}) = T_1(\mathbf{j})$, we mean

- the **identifiers** S_2 and T_1 are the same
- the **dimensions** of \mathbf{i} and \mathbf{j} are the same

Examples: $S() \neq S(i)$, $S(a) = S(b)$, $S() \neq T()$

Space Decomposition

General form can be rewritten

$$\{ S_1(\mathbf{i}) \rightarrow T_1(\mathbf{j}) : f_1(\mathbf{i}, \mathbf{j}); S_2(\mathbf{i}) \rightarrow T_2(\mathbf{j}) : f_2(\mathbf{i}, \mathbf{j}); \dots \}$$

$$= \bigcup_k \{ S_k(\mathbf{i}) \rightarrow T_k(\mathbf{j}) : f_k(\mathbf{i}, \mathbf{j}) \}$$

- some operations distribute with union
- other operations are defined on a single (pair of) space(s)
 - ⇒ “space local” operations
 - ⇒ replace $\{ S(\mathbf{i}) \rightarrow T(\mathbf{j}) : f_1(\mathbf{i}, \mathbf{j}); S(\mathbf{i}) \rightarrow T(\mathbf{j}) : f_2(\mathbf{i}, \mathbf{j}) \}$
by $\{ S(\mathbf{i}) \rightarrow T(\mathbf{j}) : f_1(\mathbf{i}, \mathbf{j}) \vee f_2(\mathbf{i}, \mathbf{j}) \}$

In both cases, we define

- unary operator \oplus

$$\oplus \bigcup_i R_i := \bigcup_i \oplus R_i$$

- binary operator \oplus

$$\left(\bigcup_i R_i \right) \oplus \left(\bigcup_j S_j \right) := \bigcup_i \bigcup_j (R_i \oplus S_j)$$

Basic Operations

$$A = \{ S(\mathbf{i}_1) \rightarrow T(\mathbf{j}_1) : f(\mathbf{i}_1, \mathbf{j}_1) \} \quad B = \{ U(\mathbf{i}_2) \rightarrow V(\mathbf{j}_2) : g(\mathbf{i}_2, \mathbf{j}_2) \}$$

- Union

$$A \cup B = \{ S(\mathbf{i}) \rightarrow T(\mathbf{j}) : f(\mathbf{i}, \mathbf{j}); U(\mathbf{i}) \rightarrow V(\mathbf{j}) : g(\mathbf{i}, \mathbf{j}) \}$$

- Intersection

$$A \cap B = \begin{cases} \{ S(\mathbf{i}) \rightarrow T(\mathbf{j}) : f(\mathbf{i}, \mathbf{j}) \wedge g(\mathbf{i}, \mathbf{j}) \} & \text{if } S(\mathbf{i}_1) = U(\mathbf{i}_2) \text{ and } T(\mathbf{j}_1) = V(\mathbf{j}_2) \\ \emptyset & \text{otherwise} \end{cases}$$

- Difference

$$A \setminus B = \begin{cases} \{ S(\mathbf{i}) \rightarrow T(\mathbf{j}) : f(\mathbf{i}, \mathbf{j}) \wedge \neg g(\mathbf{i}, \mathbf{j}) \} & \text{if } S(\mathbf{i}_1) = U(\mathbf{i}_2) \text{ and } T(\mathbf{j}_1) = V(\mathbf{j}_2) \\ \{ S(\mathbf{i}) \rightarrow T(\mathbf{j}) : f(\mathbf{i}, \mathbf{j}) \} & \text{otherwise} \end{cases}$$

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- Emptiness check
Does a set or binary relation contain any elements?
(for any value of the symbolic constants)

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- Comparisons

- ▶ $A \subseteq B$ is defined as $A \setminus B = \emptyset$
- ▶ $A \supseteq B$ is defined as $B \subseteq A$
- ▶ $A = B$ is defined as $A \subseteq B \wedge A \supseteq B$
- ▶ $A \subset B$ is defined as $A \subseteq B \wedge \neg(A = B)$
- ▶ $A \supset B$ is defined as $B \subset A$

Cardinality

- Cardinality of a set
 - ⇒ number of elements in the set
 - ⇒ may depend on symbolic constants

$$S = \{ S(\mathbf{i}) : f(\mathbf{i}) \}$$

$$\text{card } S = \{ n : n = \# \mathbf{i} : f(\mathbf{i}) \}$$

$$\text{card } \{ A(i) : 0 \leq i \leq n; B() \} = n + 2$$

$$\text{card} \left(\bigcup_i S_i \right) := \sum_i \text{card } S_i$$

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- Cardinality of a binary relation

⇒ for each domain element, number of corresponding images

$$R = \{ S(\mathbf{i}) \rightarrow T(\mathbf{j}) : f(\mathbf{i}, \mathbf{j}) \}$$

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$$R = \{ A(i) \rightarrow C(i) : 0 \leq i \leq n; B() \rightarrow C(i) : 0 \leq i \leq n \}$$

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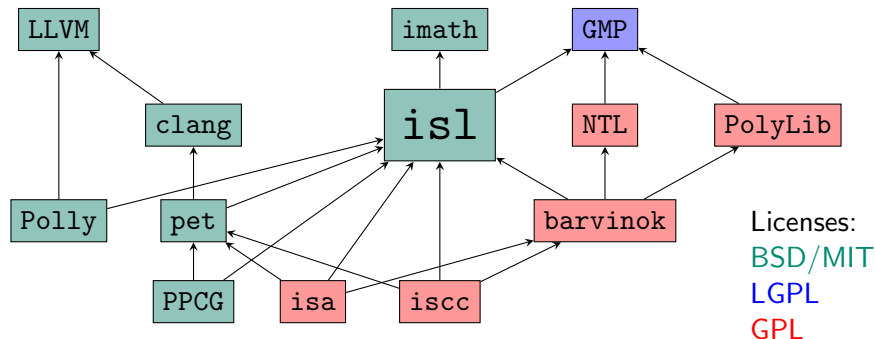
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⇒ not a Presburger formula

isl and Related Libraries and Tools



isl: manipulates parametric affine sets and relations

barvinok: counts elements in parametric affine sets and relations

pet: extracts polyhedral model from clang AST

PPCG: Polyhedral Parallel Code Generator

iscc: interactive calculator

isa: prototype tool set including derivation of process networks and equivalence checker

isl/iscc syntax

Relation description

$()$ (tuple)	$[]$
$+$, $-$	$+$, $-$
$=$, \leq , $<$, \geq , $>$	$=$, \leq , $<$, \geq , $>$
true	true
false	false
\wedge	and
\vee	or
\neg	not
$\exists v :$	exists $v :$
$\forall v :$	not exists $v :$ not
\preccurlyeq , \prec , \succcurlyeq , \succ	(not available yet; write out explicitly)

Operations on relations

\cup	$+$
\cap	$*$
\setminus	$-$
$=$	$=$
$\subset, \subseteq, \supset, \supseteq$	$<, \leq, >, \geq$
card	card

Note: symbolic constants need to be explicitly declared

Parametric Example: Matrix Multiplication

```
for (int i = 0; i < M; i++)  
  for (int j = 0; j < N; j++) {  
S1:   C[i][j] = 0;  
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S2:   C[i][j] = C[i][j] + A[i][k] * B[k][j];  
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- Number of statement instances

$$\text{card} \{ \begin{array}{l} \text{S1}(i,j) : 0 \leq i < M \wedge 0 \leq j < N; \\ \text{S2}(i,j,k) : 0 \leq i < M \wedge 0 \leq j < N \wedge 0 \leq k < K \end{array} \}$$

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- Number of array elements accessed by each instance

$$\text{card} \{ \textcolor{red}{S1}(i,j) \rightarrow \textcolor{red}{C}(i,j); \textcolor{blue}{S2}(i,j,k) \rightarrow \textcolor{blue}{C}(i,j); \\ \textcolor{blue}{S2}(i,j,k) \rightarrow \textcolor{blue}{C}(i,j); \textcolor{blue}{S2}(i,j,k) \rightarrow \textcolor{blue}{A}(i,k); \textcolor{blue}{S2}(i,j,k) \rightarrow \textcolor{blue}{B}(k,j) \}$$

Exercise

```
int f1(int m, int n, int A[const static m][n])
{
    int t = 0;
    for (int i = 0; i < m; ++i)
        t += A[i][i];
    return t;
}

void f2(int m, int n, int A[/***/m][n][n], int B[/***/m][n])
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    for (int i = 0; i < m; ++i) {
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T:        B[i][j] = f1(j, n, A[i]);
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How many statement instances are executed by f2? $m > n ? m * n - n + m : m * n$

How many array elements accessed by each instance? $S[i] \rightarrow 1; T[i, j] \rightarrow 1 + j$

Outline

1 Polyhedral Model

- Introduction
- Representation

2 Polyhedral Transformation

- Schedules
- AST Generation

3 Dependences

- Schedule Validity
- Dependences
- Structures

4 Dataflow

- Parallelism
- Dataflow
- Approximate Dataflow
- Run-time dependent Dataflow
- Reductions

5 Aliasing

6 Counting

- Cardinality
- Bounds
- Weighted Counting
- Dynamic Memory Requirement

7 Transitive Closures

Polyhedral Transformation

Two approaches

- ① encode execution order in statement instance indices
 - ⇒ transformation performed by manipulating instance set
- ② keep track of execution order separately: schedule
 - ⇒ transformation performed by manipulating initial/current schedule, or
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Polyhedral Transformation

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- ② **keep track of execution order separately: schedule**
 - ⇒ transformation performed by manipulating initial/current schedule, or
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Schedule O keeps track of relative execution order of statement instances

⇒ for each pair of statement instances $S(i)$ and $T(j)$,
schedule determines

- | | |
|--|---------------------|
| ▶ $S(i)$ executed before $T(j)$ | $O(S(i), T(j)) < 0$ |
| ▶ $S(i)$ executed after $T(j)$ | $O(S(i), T(j)) > 0$ |
| ▶ $S(i)$ and $T(j)$ may be executed simultaneously | $O(S(i), T(j)) = 0$ |

Schedule Representations

Types of schedule representations

- Combined representations
 - ▶ schedule tree
 - ▶ named Presburger relation
- Scattered representations
 - ▶ Kelly's abstraction
 - ▶ " $2d + 1$ "

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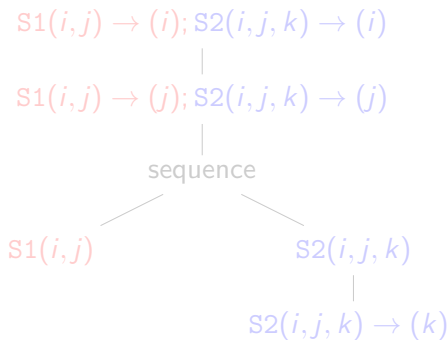
- Combined representations
 - ▶ **schedule tree**
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Schedule Trees

- Main node types
 - ▶ sequence: children are executed in order
 - ▶ band: instance are executed according to associated piecewise quasi-affine partial schedule P
- Deriving schedule tree from AST
 - ▶ for loop \Rightarrow single-dimensional band
 - ▶ compound statement \Rightarrow sequence

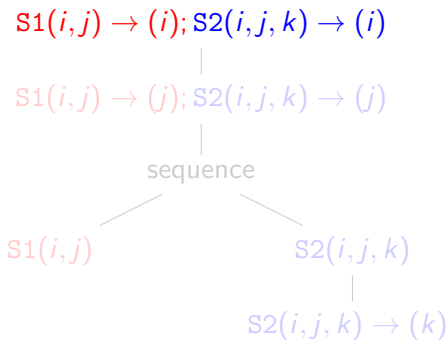
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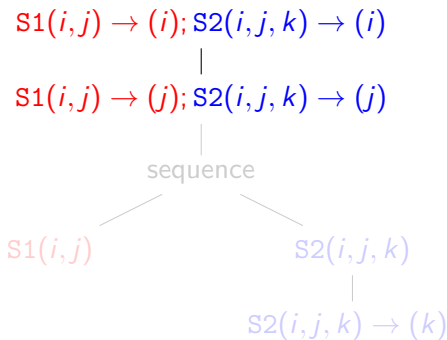
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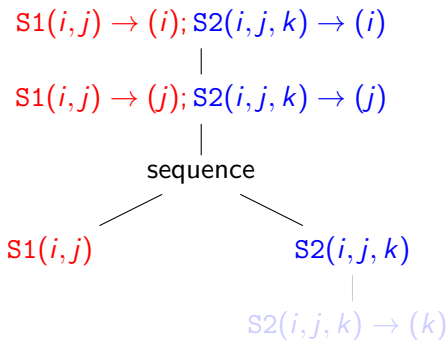
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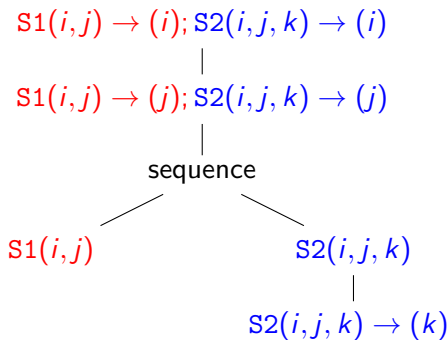
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S2:    C[i][j] = C[i][j] + A[i][k] * B[k][j];  
  }  
}
```



Schedule Trees — Execution Order

What is the execution order of statement instances $S(i)$ and $T(j)$ determined by schedule tree O ?

Start at root of schedule tree

```

while current node is not a leaf do
  if current node is sequence then
    if  $S(i)$  and  $T(j)$  appear in same child then
      | Move to common child
    else if  $S(i)$  appears in earlier child then
      | return  $O(S(i), T(j)) < 0$ 
    else
      | return  $O(S(i), T(j)) > 0$ 
  else
    if  $P(S(i)) = P(T(j))$  then
      | Move to single child
    else if  $P(S(i)) \prec P(T(j))$  then
      | return  $O(S(i), T(j)) < 0$ 
    else
      | return  $O(S(i), T(j)) > 0$ 
return  $O(S(i), T(j)) = 0$ 
  
```

Named Presburger Relation Schedules

Schedule tree with single (band) node

Named Presburger Relation Schedules

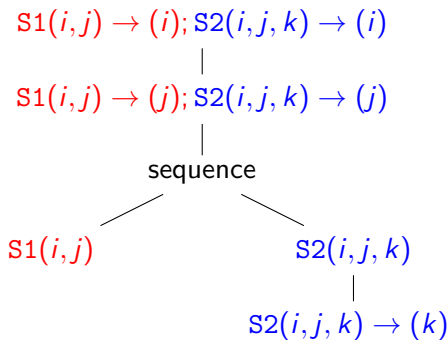
Schedule tree with single (band) node

Flattening a schedule tree

- two nested band nodes
 - ⇒ replace by single band node with concatenated partial schedule
- sequence with as children either leaves or trees consisting of a single band node
 - ⇒ treat leaves as zero-dimensional band nodes
 - ⇒ pad lower-dimensional bands (e.g., with zero)
 - ⇒ construct one-dimensional band assigning increasing value to children
 - ⇒ combine one-dimensional band with children

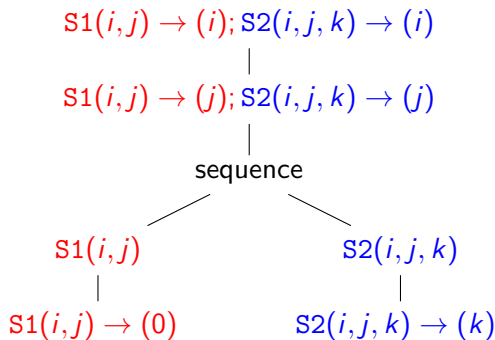
Parametric Example: Matrix Multiplication

```
for (int i = 0; i < M; i++)  
  for (int j = 0; j < N; j++) {  
S1:    C[i][j] = 0;  
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  }
```

$$S1(i, j) \rightarrow (i); S2(i, j, k) \rightarrow (i)$$
$$S1(i, j) \rightarrow (j); S2(i, j, k) \rightarrow (j)$$
$$S1(i, j) \rightarrow (0, 0); S2(i, j, k) \rightarrow (1, k)$$

Parametric Example: Matrix Multiplication

```
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  for (int j = 0; j < N; j++) {  
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  }
```

$$S1(i, j) \rightarrow (i); S2(i, j, k) \rightarrow (i)$$
$$S1(i, j) \rightarrow (j, 0, 0); S2(i, j, k) \rightarrow (j, 1, k)$$

Parametric Example: Matrix Multiplication

```
for (int i = 0; i < M; i++)  
  for (int j = 0; j < N; j++) {  
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S2:      C[i][j] = C[i][j] + A[i][k] * B[k][j];  
  }
```

$S1(i,j) \rightarrow (i,j,0,0); S2(i,j,k) \rightarrow (i,j,1,k)$

Domain and Range of a Relation

$$R = \{ S(\mathbf{i}) \rightarrow T(\mathbf{j}) : f(\mathbf{i}, \mathbf{j}) \}$$

- Domain

(iscc: dom)

$$\text{dom } R = \{ S(\mathbf{i}) : \exists \mathbf{j} : f(\mathbf{i}, \mathbf{j}) \}$$

$$W = \{ S(i, j) \rightarrow A(i + j) : 0 \leq i < 2 \wedge 0 \leq j < 2 \}$$

$$\text{dom } W = \{ S(i, j) : 0 \leq i < 2 \wedge 0 \leq j < 2 \}$$

\Rightarrow statement instances writing any array element

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$$\text{dom } W = \{ S(i, j) : 0 \leq i < 2 \wedge 0 \leq j < 2 \}$$

\Rightarrow statement instances writing any array element

- Range (iscc: ran)

$$\text{ran } R = \{ T(\mathbf{j}) : \exists \mathbf{i} : f(\mathbf{i}, \mathbf{j}) \}$$

$$W = \{ S(i, j) \rightarrow A(i + j) : 0 \leq i < 2 \wedge 0 \leq j < 2 \}$$

$$\text{ran } W = \{ A(a) : 0 \leq a \leq 2 \}$$

\Rightarrow written array elements

Domain/Range Restriction

$$A = \{ S_1(\mathbf{i}_1) : f(\mathbf{i}_1) \}$$

$$B = \{ T_1(\mathbf{j}_1) : g(\mathbf{j}_1) \}$$

$$C = \{ S_2(\mathbf{i}_2) \rightarrow T_2(\mathbf{j}_2) : h(\mathbf{i}_2, \mathbf{j}_2) \}$$

- Product relation

(iscc: \rightarrow)

$$A \rightarrow B = \{ S_1(\mathbf{i}) \rightarrow T_1(\mathbf{j}) : f(\mathbf{i}) \wedge g(\mathbf{j}) \}$$

Domain/Range Restriction

$$A = \{ S_1(\mathbf{i}_1) : f(\mathbf{i}_1) \}$$

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- Product relation (iscc: \rightarrow)

$$A \rightarrow B = \{ S_1(\mathbf{i}) \rightarrow T_1(\mathbf{j}) : f(\mathbf{i}) \wedge g(\mathbf{j}) \}$$

- Domain restriction (iscc: $*$)

$$R \cap_{\text{dom}} S = R \cap (S \rightarrow (\text{ran } R))$$

$$C \cap_{\text{dom}} A = \begin{cases} \{ S_2(\mathbf{i}) \rightarrow T_2(\mathbf{j}) : f(\mathbf{i}) \wedge h(\mathbf{i}, \mathbf{j}) \} & \text{if } S_1(\mathbf{i}_1) = S_2(\mathbf{i}_2) \\ \emptyset & \text{otherwise} \end{cases}$$

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- Range restriction

$$R \cap_{\text{ran}} S = R \cap ((\text{dom } R) \rightarrow S)$$

$$C \cap_{\text{ran}} A = \begin{cases} \{ S_2(\mathbf{i}) \rightarrow T_2(\mathbf{j}) : f(\mathbf{j}) \wedge h(\mathbf{i}, \mathbf{j}) \} & \text{if } S_1(\mathbf{i}_1) = T_2(\mathbf{j}_2) \\ \emptyset & \text{otherwise} \end{cases}$$

AST Generation

[5, 6]

Input: • instance set
 • schedule

Output: • AST that visits each domain element according to the
 order specified by the schedule

Note: in case of flat schedule, schedule order is lexicographic order of output space

⇒ single output space

iscc codegen operation takes as input flat schedule with instance set encoded in domain

⇒ apply * to “pure” schedule and instance set first

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  }  
  
I := [M,N,K] -> { S1[i,j] : 0 <= i < M and 0 <= j < N;  
  S2[i,j,k] : 0 <= i < M and 0 <= j < N and 0 <= k < K };  
O := { S1[i,j] -> [i,j,0,0]; S2[i,j,k] -> [i,j,1,k] };  
codegen (O * I);
```

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O := { S1[i,j] -> [i,j,0,0]; S2[i,j,k] -> [i,j,1,k] };  
codegen (O * I);
```

```
for (int c0 = 0; c0 < M; c0 += 1)  
  for (int c1 = 0; c1 < N; c1 += 1) {  
    S1(c0, c1);  
    for (int c3 = 0; c3 < K; c3 += 1)  
      S2(c0, c1, c3);  
  }
```

Exercise

```
int f1(int m, int n, int A[const static m][n])
{
    int t = 0;
    for (int i = 0; i < m; ++i)
        t += A[i][i];
    return t;
}

void f2(int m, int n, int A[/***/m][n][n], int B[/***/m][n])
{
    for (int i = 0; i < m; ++i) {
S:        B[i][0] = 0;
        for (int j = 0; j < n; ++j) {
            if (j == i)
                continue;
T:        B[i][j] = f1(j, n, A[i]);
        }
    }
}
```

Write down schedule and generate AST

codegen (0 * I)

Outline

1 Polyhedral Model

- Introduction
- Representation

2 Polyhedral Transformation

- Schedules
- AST Generation

3 Dependencies

- Schedule Validity
- Dependencies
- Structures

4 Dataflow

- Parallelism
- Dataflow
- Approximate Dataflow
- Run-time dependent Dataflow
- Reductions

5 Aliasing

6 Counting

- Cardinality
- Bounds
- Weighted Counting
- Dynamic Memory Requirement

7 Transitive Closures

Schedule Validity

[1]

Not all schedules correspond to a valid execution order

Schedule Validity

[1]

Not all schedules correspond to a valid execution order

$R(a) \quad W(a) \longrightarrow R(a) \quad W(b) \quad W(a) \quad W(a)$

Internal restrictions

- A read of a value should not be scheduled after the write of the value
- No other write to same memory location may be scheduled in between

External restrictions (on non-temporaries)

- No write may be scheduled before initial read from a memory location
- No write may be scheduled after last write to a memory location

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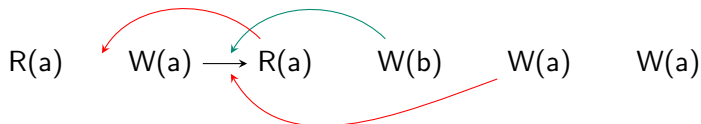
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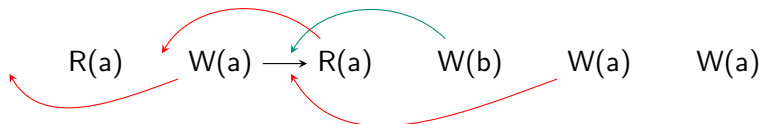
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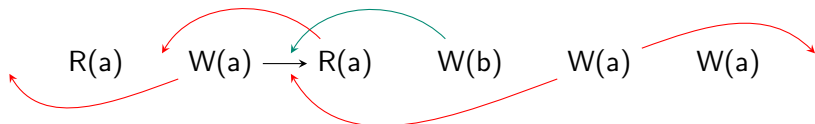
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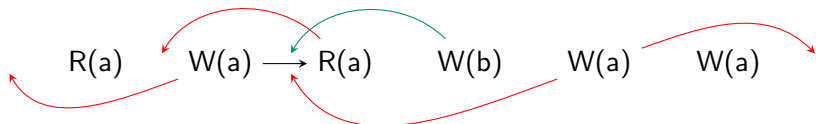
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Sufficient conditions:

- Every read of a memory location is scheduled after every previous write to the same memory location
- Every write to a memory location is scheduled after every previous read or write to the same memory location

Dependences

Sufficient conditions for schedule validity:

- Every read of a memory location is scheduled after every previous write to the same memory location
- Every write to a memory location is scheduled after every previous read or write to the same memory location

Dependence relation D : pairs of statement instances

- accessing the same memory location
- of which at least one is a write
- with the first executed before the second

Sufficient condition:

$$\forall S(i) \rightarrow T(j) \in D : O(S(i), T(j)) < 0$$

Inverse Relation and Composition

$$A = \{ S_1(\mathbf{i}_1) \rightarrow T_1(\mathbf{j}_1) : f(\mathbf{i}_1, \mathbf{j}_1) \} \quad B = \{ S_2(\mathbf{i}_2) \rightarrow T_2(\mathbf{j}_2) : g(\mathbf{i}_2, \mathbf{j}_2) \}$$

- Inverse (iscc: $\hat{-1}$)

$$A^{-1} = \{ T_1(\mathbf{j}) \rightarrow S_1(\mathbf{i}) : f(\mathbf{i}, \mathbf{j}) \}$$

$$W = \{ S(i, j) \rightarrow A(i + j) : 0 \leq i < 2 \wedge 0 \leq j < 2 \}$$

$$W^{-1} = \{ A(a) \rightarrow S(i, j) : a = i + j \wedge 0 \leq i < 2 \wedge 0 \leq j < 2 \}$$

\Rightarrow statement instances writing array element

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$$W^{-1} = \{ A(a) \rightarrow S(i, j) : a = i + j \wedge 0 \leq i < 2 \wedge 0 \leq j < 2 \}$$

\Rightarrow statement instances writing array element

- Composition (iscc: after)

$$B \circ A = \begin{cases} \{ S_1(\mathbf{i}) \rightarrow T_2(\mathbf{j}) : \exists \mathbf{k} : f(\mathbf{i}, \mathbf{k}) \wedge g(\mathbf{k}, \mathbf{j}) \} & \text{if } T_1(\mathbf{j}_1) = S_2(\mathbf{i}_2) \\ \emptyset & \text{otherwise} \end{cases}$$

$$W^{-1} \circ W = \{ S(i, j) \rightarrow S(i', j') : 0 \leq i, i', j, j' < 2 \wedge i + j = i' + j' \}$$

\Rightarrow pairs of statement instances that write same array element

Lexicographic Order

- Sets

(iscc: <<)

$$A = \{ S(\mathbf{i}) : f(\mathbf{i}) \}$$

$$B = \{ T(\mathbf{j}) : g(\mathbf{j}) \}$$

$$A \prec B = \begin{cases} \{ S(\mathbf{i}) \rightarrow S(\mathbf{j}) : f(\mathbf{i}) \wedge g(\mathbf{j}) \wedge \mathbf{i} \prec \mathbf{j} \} & \text{if } S(\mathbf{i}) = T(\mathbf{j}) \\ \emptyset & \text{otherwise} \end{cases}$$

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- Relations (iscc: <<)

⇒ binary relation on domains reflecting lexicographic order of images

$$A = \{ S_1(\mathbf{i}_1) \rightarrow T_1(\mathbf{j}_1) : f(\mathbf{i}_1, \mathbf{j}_1) \}$$

$$B = \{ S_2(\mathbf{i}_2) \rightarrow T_2(\mathbf{j}_2) : g(\mathbf{i}_2, \mathbf{j}_2) \}$$

$$A \prec B = \begin{cases} \{ S_1(\mathbf{i}_1) \rightarrow S_2(\mathbf{i}_2) : \exists \mathbf{j}_1, \mathbf{j}_2 : f(\mathbf{i}_1, \mathbf{j}_1) \wedge g(\mathbf{i}_2, \mathbf{j}_2) \wedge \mathbf{j}_1 \prec \mathbf{j}_2 \} & \text{if } T_1(\mathbf{j}_1) = T_2(\mathbf{j}_2) \\ \emptyset & \text{otherwise} \end{cases}$$

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0 := { S1[i,j] -> [i,j,0,0]; S2[i,j,k] -> [i,j,1,k] };  
0 << 0;
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0 := { S1[i,j] -> [i,j,0,0]; S2[i,j,k] -> [i,j,1,k] };  
0 << 0;  
  
{ S2[i, j, k] -> S2[i', j', k'] : i' >= 1 + i;  
  S2[i, j, k] -> S2[i, j', k'] : j' >= 1 + j;  
  S2[i, j, k] -> S2[i, j, k'] : k' >= 1 + k;  
  S1[i, j] -> S2[i', j', k] : i' >= 1 + i;  
  S1[i, j] -> S2[i, j', k] : j' >= 1 + j;  
  S1[i, j] -> S2[i, j, k]; S2[i, j, k] -> S1[i', j'] : i' >= 1 + i;  
  S2[i, j, k] -> S1[i, j'] : j' >= 1 + j;  
  S1[i, j] -> S1[i', j'] : i' >= 1 + i;  
  S1[i, j] -> S1[i, j'] : j' >= 1 + j }
```

Dependence Analysis

Recall: sufficient condition for schedule validity

$$\forall S(\mathbf{i}) \rightarrow T(\mathbf{j}) \in D : O(S(\mathbf{i}), T(\mathbf{j})) < 0$$

Dependence relation D : pairs of statement instances

- accessing the same memory location
- of which at least one is a write
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Dependence Analysis

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Dependence relation D : pairs of statement instances

- accessing the same memory location
- of which at least one is a write
- with the first executed before the second

Computation:

$$D = ((W^{-1} \circ R) \cup (W^{-1} \circ W) \cup (R^{-1} \circ W)) \cap (O \prec O)$$

W : write access relation

R : read access relation

O : original schedule

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    for (int j = 0; j < N; j++) {  
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    }
```

$W := \{ S1[i,j] \rightarrow C[i,j]; S2[i,j,k] \rightarrow C[i,j] \};$

$R := \{ S2[i,j,k] \rightarrow C[i,j]; S2[i,j,k] \rightarrow A[i,k];$
 $S2[i,j,k] \rightarrow B[k,j] \};$

$I := [M,N,K] \rightarrow \{ S1[i,j] : 0 \leq i < M \text{ and } 0 \leq j < N;$
 $S2[i,j,k] : 0 \leq i < M \text{ and } 0 \leq j < N \text{ and } 0 \leq k < K \};$

$O := \{ S1[i,j] \rightarrow [i,j,0,0]; S2[i,j,k] \rightarrow [i,j,1,k] \};$

$((R \cdot W^{-1}) + (W \cdot W^{-1}) + (W \cdot R^{-1})) * (O \ll 0);$

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S2:      C[i][j] = C[i][j] + A[i][k] * B[k][j];  
  }  
  
W := { S1[i,j] -> C[i,j]; S2[i,j,k] -> C[i,j] };  
R := { S2[i,j,k] -> C[i,j]; S2[i,j,k] -> A[i,k];  
        S2[i,j,k] -> B[k,j] };  
I := [M,N,K] -> { S1[i,j] : 0 <= i < M and 0 <= j < N;  
        S2[i,j,k] : 0 <= i < M and 0 <= j < N and 0 <= k < K };  
O := { S1[i,j] -> [i,j,0,0]; S2[i,j,k] -> [i,j,1,k] };  
( (R . W^-1) + (W . W^-1) + (W . R^-1) ) * (O << O);  
  
{ S2[i, j, k] -> S2[i, j, k'] : k' >= 1 + k;  
  S1[i, j] -> S2[i, j, k] }
```

Exercise

```
int f1(int m, int n, int A[const static m][n])
{
    int t = 0;
    for (int i = 0; i < m; ++i)
        t += A[i][i];
    return t;
}

void f2(int m, int n, int A[/* */m][n][n], int B[/* */m][n])
{
    for (int i = 0; i < m; ++i) {
S:        B[i][0] = 0;
        for (int j = 0; j < n; ++j) {
            if (j == i)
                continue;
T:        B[i][j] = f1(j, n, A[i]);
        }
    }
}
```

Compute dependence relation

Exercise

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Compute dependence relation

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Accesses to Structure Fields and Nested Relations

No special treatment is needed for representing accesses to structure fields

⇒ structure field encoded in name of target space of
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Accesses to Structure Fields and Nested Relations

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```
struct s {  
    int a;  
    int b[10];  
};
```

```
void f(struct s s[const static 10][10])  
{  
    for (int i = 0; i < 10; ++i)  
S:      s[i][i].b[9 - i] = 0;  
}
```

$\{ S(i) \rightarrow s_b(i, i, 9 - i) \}$

Accesses to Structure Fields and Nested Relations

No special treatment is needed for representing accesses to structure fields

⇒ structure field encoded in name and/or structure of target space of access relations

```
struct s {  
    int a;  
    int b[10];  
};
```

```
void f(struct s s[const static 10][10])  
{  
    for (int i = 0; i < 10; ++i)  
S:      s[i][i].b[9 - i] = 0;  
}
```

$\{ S(i) \rightarrow s_b(i, i, 9 - i) \}$

In pet, structure encoded in *nested relation*

$\{ S(i) \rightarrow s_b(s(i, i) \rightarrow b(9 - i)) \}$

Accesses to Structures

Dependence analysis needs to take into account that access to structure represents access to all fields of structure

```
struct c {  
    float re;  
    float im;  
};  
  
void f(struct c A[const static 10])  
{  
S:    A[0] = A[2];  
T:    A[1].re = A[0].im;  
}
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Write access relation: $\{ S() \rightarrow A(0); T() \rightarrow A_re(A(1) \rightarrow re()) \}$

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Write access relation: $\{ S() \rightarrow A(0); T() \rightarrow A_re(A(1) \rightarrow re()) \}$

Expansion: $\{ A(a) \rightarrow A_re(A(a) \rightarrow re()); A(a) \rightarrow A_im(A(a) \rightarrow im()); \}$

Expanded write access relation:

$$\{ S() \rightarrow A_re(A(0) \rightarrow re()); S() \rightarrow A_im(A(0) \rightarrow im()); \\ T() \rightarrow A_re(A(1) \rightarrow re()) \}$$

Outline

1 Polyhedral Model

- Introduction
- Representation

2 Polyhedral Transformation

- Schedules
- AST Generation

3 Dependences

- Schedule Validity
- Dependences
- Structures

4 Dataflow

- Parallelism
- Dataflow
- Approximate Dataflow
- Run-time dependent Dataflow
- Reductions

5 Aliasing

6 Counting

- Cardinality
- Bounds
- Weighted Counting
- Dynamic Memory Requirement

7 Transitive Closures

Schedule Optimization Criteria

Typical optimization criteria

- increase parallelism
- increase locality
- reduce memory requirements

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Typical optimization criteria

- increase parallelism
- increase locality
- reduce memory requirements

Parallelism:

Pairs of statement instances $S(\mathbf{i})$ and $T(\mathbf{j})$ may be executed in parallel if they do not depend on each other:

$$\{ S(\mathbf{i}) \rightarrow T(\mathbf{j}); T(\mathbf{j}) \rightarrow S(\mathbf{i}) \} \cap D = \emptyset$$

Local Parallelism

Global parallelism

$$\{ S(\mathbf{i}) \rightarrow T(\mathbf{j}); T(\mathbf{j}) \rightarrow S(\mathbf{i}) \} \cap D = \emptyset$$

Local parallelism

$$\{ S(\mathbf{i}) \rightarrow T(\mathbf{j}); T(\mathbf{j}) \rightarrow S(\mathbf{i}) \} \cap L = \emptyset$$

- Root of schedule tree: $L = D$
- Child of band node b with partial schedule P :
$$L = L_b \cap \{ S(\mathbf{i}) \rightarrow T(\mathbf{j}) : P(S(\mathbf{i})) = P(T(\mathbf{j})) \}$$
- Child of sequence node s with instance set F
$$L = L_s \cap \{ S(\mathbf{i}) \rightarrow T(\mathbf{j}) : S(\mathbf{i}) \in F \wedge T(\mathbf{j}) \in F \}$$

Encoding Parallelism

- Coarse grain parallelism (root)
 - ▶ *placement* maps statement instances to virtual processors
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Encoding Parallelism

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- Fine grain parallelism (leaf)
 - ▶ statement instances $S(i)$ and $T(j)$ for which $O(S(i), T(j)) = 0$ may be executed in parallel within leaf that contains both
- General case (arbitrary position in schedule tree)
 - ▶ schedules defines total order, but,
 - ▶ some sequence nodes and/or some band dimensions are explicitly marked parallel

False Dependences

```
for (int i = 0; i < n; ++i) {  
  S:      t = f1(A[i]);  
  T:      B[i] = f2(t);  
}
```

Dependences

- read after write (“true”):
- write after read (“anti”):
- write after write (“output”):

$$\{ S(i) \rightarrow T(i') : i' \geq i \}$$

$$\{ T(i) \rightarrow S(i') : i' > i \}$$

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False dependences not from dataflow, but from reuse of memory location t

Possible solution: expansion/privatization

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Lexicographic Optimization (Space Local)

- Lexicographic Minimum of Sets

(iscc: lexmin)

$$S = \{ S(i) : f(i) \}$$

$$\text{lexmin } S = \{ S(i) : f(i) \wedge \forall i' : f(i') \Rightarrow i \preceq i' \}$$

$$I = \{ R(); S(i, j) : 0 \leq i < 2 \wedge 0 \leq j < 2; T(k) : 0 \leq k < 2 \}$$

$$\text{lexmin } I = \{ R(); S(0, 0); T(0) \}$$

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- Lexicographic Maximum of Relations (iscc: lexmax)

$$R = \{ S(i) \rightarrow T(j) : f(i, j) \}$$

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$$W^{-1} = \{ A(a) \rightarrow S(i, j) : a = i + j \wedge 0 \leq i < 2 \wedge 0 \leq j < 2 \}$$

$$\text{lexmax}(W^{-1}) = \{ A(a) \rightarrow S(a, 0) : 0 \leq a \leq 1; A(2) \rightarrow S(1, 1) \}$$

\Rightarrow last statement instance writing array element

Array Dataflow Analysis

Given a read from an array element, what was the last write to the same array element before the read?

```
for (i = 0; i < N; ++i)
    for (j = 0; j < N - i; ++j)
F:      a[i+j] = f(a[i+j]);
for (i = 0; i < N; ++i)
W:      Write(a[i]);
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```

Access relations:

$$A_1 = \{ F(i, j) \rightarrow a(i+j) : 0 \leq i < N \wedge 0 \leq j < N - i \}$$

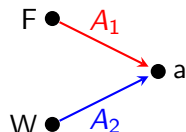
$$A_2 = \{ W(i) \rightarrow a(i) : 0 \leq i < N \}$$

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Map to all writes:

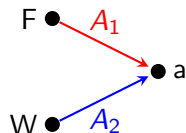
$$R' = A_1^{-1} \circ A_2 = \{ W(i) \rightarrow F(i', i - i') : 0 \leq i' \leq i < N \}$$

Array Dataflow Analysis

Given a read from an array element, what was the *last* write to the same array element before the read?

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for (i = 0; i < N; ++i)
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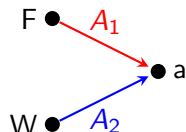
Array Dataflow Analysis

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In general: impose lexicographical order on shared branch of schedule tree

Expansion

Assume:

- instance sets and access relations are static and exact
⇒ each read has exactly one corresponding write
- single read and write per statement
⇒ expanded array indexed by statement instance of write

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Dataflow: $\{S(i) \rightarrow T(i)\}$

```
for (int i = 0; i < n; ++i) {  
S:      S[i] = f1(A[i]);  
T:      B[i] = f2(S[i]);  
}
```

⇒ only remaining dependences are dataflow induced

Tagged Access Relations

Expansion in case of multiple reads or writes per statement

- ⇒ statement instance not enough to identify memory access
- ⇒ use identifier of array reference instead

Tagged Access Relations

Expansion in case of multiple reads or writes per statement

- ⇒ statement instance not enough to identify memory access
- ⇒ use identifier of array reference instead
- ⇒ pet: embed array reference in “tagged” instance set
- ⇒ “ternary” access relations

$$I \rightarrow R \rightarrow A$$

I : statement instance

R : reference identifier

O : array element

- ⇒ in practice: nested binary relation

$$(I \rightarrow R) \rightarrow A$$

For example, $\{ (S(i) \rightarrow R1()) \rightarrow A(i) \}$

Wrapping, Unwrapping, Domain Map and Range Map

$$R = \{ S(\mathbf{i}) \rightarrow T(\mathbf{j}) : f(\mathbf{i}, \mathbf{j}) \} \quad S = \{ (U(\mathbf{i}) \rightarrow V(\mathbf{j})) : g(\mathbf{i}, \mathbf{j}) \}$$

- Wrap (iscc: wrap)

$$\mathcal{W}R = \{ (S(\mathbf{i}) \rightarrow T(\mathbf{j})) : f(\mathbf{i}, \mathbf{j}) \}$$

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- Domain map (iscc: domain_map)

$$\underline{\text{dom}} R = \{ (S(\mathbf{i}) \rightarrow T(\mathbf{j})) \rightarrow S(\mathbf{i}) : f(\mathbf{i}, \mathbf{j}) \}$$

$$I = \{ (S(i) \rightarrow R1()) : 0 \leq i < n \}$$

$$\underline{\text{dom}}(\mathcal{W}^{-1}I) = \{ (S(i) \rightarrow R1()) \rightarrow S(i) : 0 \leq i < n \}$$

- ⇒ maps tagged instance set to untagged instance set
- ⇒ precompose with instance set based relations to obtain tagged instance set based relations

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- Range map (iscc: range_map)

$$\underline{\text{ran}} R = \{ (S(\mathbf{i}) \rightarrow T(\mathbf{j})) \rightarrow T(\mathbf{j}) : f(\mathbf{i}, \mathbf{j}) \}$$

Parametric Example: Matrix Multiplication

```

for (int i = 0; i < M; i++)
  for (int j = 0; j < N; j++) {
S1:    C[i][j] = 0;
        for (int k = 0; k < K; k++)
S2:      C[i][j] = C[i][j] + A[i][k] * B[k][j];
  }

```

- Tagged Access Relations

$$\begin{aligned}
 W &= \{ (S1(i, j) \rightarrow R0()) \rightarrow C(i, j); (S2(i, j, k) \rightarrow R1()) \rightarrow C(i, j) \} \\
 R &= \{ (S2(i, j, k) \rightarrow R2()) \rightarrow C(i, j); (S2(i, j, k) \rightarrow R3()) \rightarrow A(i, k); \\
 &\quad (S2(i, j, k) \rightarrow R4()) \rightarrow B(k, j) \}
 \end{aligned}$$

- Tagged Schedule

$$\begin{aligned}
 &\{ (S1(i, j) \rightarrow R0()) \rightarrow (i, j, 0, 0); (S2(i, j, k) \rightarrow R1()) \rightarrow (i, j, 1, k) \\
 &\quad (S2(i, j, k) \rightarrow R2()) \rightarrow (i, j, 1, k); (S2(i, j, k) \rightarrow R3()) \rightarrow (i, j, 1, k); \\
 &\quad (S2(i, j, k) \rightarrow R4()) \rightarrow (i, j, 1, k) \}
 \end{aligned}$$

- Tagged Dataflow

$$\begin{aligned}
 &\{ (S1(i, j) \rightarrow R0()) \rightarrow (S2(i, j, 0) \rightarrow R2()) \\
 &\quad (S2(i, j, k) \rightarrow R1()) \rightarrow (S2(i, j, k + 1) \rightarrow R2()) \}
 \end{aligned}$$

Maximal Static Expansion

[3]

```
for (int i = 0; i < n; ++i) {  
S1:    t = f1(i);  
S2:    A[i] = t;  
S3:    t = f2(i);  
S4:    if (f3(i))  
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Dataflow cannot be determined independently of run-time information

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```

Dataflow cannot be determined independently of run-time information

⇒ approximate dataflow

$$\{ S1(i) \rightarrow S2(i); S3(i) \rightarrow S6(i); S5(i) \rightarrow S6(i) \}$$

Maximal Static Expansion

[3]

```
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S3:    t = f2(i);  
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```

Dataflow cannot be determined independently of run-time information

⇒ approximate dataflow

$\{ S1(i) \rightarrow S2(i); S3(i) \rightarrow S6(i); S5(i) \rightarrow S6(i) \}$

⇒ a read may be associated to more than one write

⇒ corresponding equivalence classes should not be expanded apart

Maximal Static Expansion

[3]

```
for (int i = 0; i < n; ++i) {  
S1:      t = f1(i);           t1[i] = f1(i);  
S2:      A[i] = t;           A[i] = t1[i];  
S3:      t = f2(i);           t2[i] = f2(i);  
S4:      if (f3(i))           if (f3(i))  
S5:          t = f4(i);         t2[i] = f4(i);  
S6:      B[i] = t;           B[i] = t2[i];  
}
```

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Approximate Dataflow Analysis

How to compute dataflow in presence of data dependent control?

Two approaches

- Direct computation
 - ▶ distinguish between may- and must-writes

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May Writes

Keep track of whether write is possible or definite

- Must-writes
Array elements that are definitely accessed by statement instance
- May-writes
Array elements that are possibly accessed by statement instance

Must-write access relation is subset of may-write access relation

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Array elements that are definitely accessed by statement instance

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Array elements that are possibly accessed by statement instance

- ▶ statement instance not necessarily executed

```
for (i = 0; i < n; ++i)
```

```
    if (A[i] > 0)
```

```
        S:      B[i] = A[i];
```

```
May-write: { S(i) → B(i) }
```

Must-write access relation is subset of may-write access relation

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```
    S:      B[i] = A[i];
```

```
    May-write: { S(i) → B(i) }
```

- ▶ array element not necessarily accessed

```
int A[N];
```

```
/* ... */
```

```
T:  A[B[0]] = 5;
```

```
May-write: { T() → A(a) : 0 ≤ a < N }
```

Must-write access relation is subset of may-write access relation

Approximate Dataflow — Direct Computation

- Read after write dependences
 - ▶ write and read access same memory location
 - ▶ write executed before the read

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 - ▶ no intermediate write to same memory location
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Approximate Dataflow — Direct Computation

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⇒ Approximate dataflow analysis with no must-writes

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Performing Dataflow Analysis

[9, 17]

Two possibilities

- 1 dataflow analysis on polyhedral model
 - ▶ first extract instance set, access relations and schedule
 - ▶ then perform dataflow analysis

E.g., isl

- 2 dataflow analysis on AST before/during model extraction
Proposed by, e.g., Maslov (1994)

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- 2 dataflow analysis on AST before/during model extraction
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Dataflow in Parallel Programs

- 1 use refined execution order during dataflow analysis
- 2 remove spurious dependences after dataflow analysis

Note: dataflow from must-writes cannot be removed without caution

- ⇒ must-write may have killed other dependences
- ⇒ other dependences may have to be added back

Explicit Kills

```
int A[N];
```

```
S:  A[0] = 1;
```

```
    for (int i = 0; i < N; ++i)
```

```
T:      A[perm[i]] = f(i);
```

```
U:  f(A[0]);
```

- Assume perm represents a permutation
⇒ there can be no dataflow from S to U

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- Assume perm represents a permutation
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- Compiler does not know all elements are written by T
 - ⇒ may find dataflow from S to U
 - ⇒ user can insert explicit kill
 - ⇒ explicit kill used to kill dependences, just like must-write

Explicit Kills

```
int A[N];
```

```
S:  A[0] = 1;
```

```
    __pencil_kill(A);
```

```
    for (int i = 0; i < N; ++i)
```

```
T:      A[perm[i]] = f(i);
```

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U:  f(A[0]);
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Local Variables and Kills

```
for (int i = 0; i < N; ++i) {  
    int t;  
    /* ... */  
}
```

- ⇒ there can be no dataflow on `t` across different iterations
- ⇒ pet automatically inserts kills
 - ▶ before declaration and
 - ▶ at end of block containing declaration

Killing False Dependences

Dataflow derived from read after write dependences through killing.

Should we do the same for false dependences?

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Optimizing locality

- typical criterion: minimize maximal dependence distance

dependence distances: $\{ P(S(\mathbf{i})) - P(T(\mathbf{j})) : S(\mathbf{i}) \rightarrow T(\mathbf{j}) \in D \}$

D : (local) dependence relation, P : partial schedule

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- typical criterion: minimize maximal dependence distance
dependence distances: $\{ P(S(\mathbf{i})) - P(T(\mathbf{j})) : S(\mathbf{i}) \rightarrow T(\mathbf{j}) \in D \}$
 D : (local) dependence relation, P : partial schedule
- may also be useful for false dependences if no expansion is performed
 - ⇒ locality of reused memory location
- killing false dependences avoids critical path determined by transitively covered dependences
 - ⇒ allow must-writes to kill dependences, but not explicit kills

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Run-time Dependent Dataflow Analysis

[4, 25]

Approaches

- “fuzzy array dataflow analysis”
- “on-demand-parametric array dataflow analysis”

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- Run-time dependent dataflow

$$\{ S1(i) \rightarrow S2(i); S3(i) \rightarrow S6(i) : \beta_{S6}^{S5} = 0; S5(i) \rightarrow S6(i) : \beta_{S6}^{S5} = 1 \}$$

β_C^P : any potential source instance P is executed for sink C

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- Approximate dataflow (project out β and λ)

$$\{ S1(i) \rightarrow S2(i); S3(i) \rightarrow S6(i); S5(i) \rightarrow S6(i) \}$$

Representing Dynamic Conditions

```
N1: n = f();  
    for (int k = 0; k < 100; ++k) {  
M:    m = g();  
        for (int i = 0; i < m; ++i)  
            for (int j = 0; j < n; ++j)  
A:                a[j][i] = g();  
N2:    n = f();  
    }
```

What is instance set (restricted to A statement)?

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N1: n = f();  
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\Rightarrow no, m and n cannot be treated as symbolic constants
(they are modified inside k-loop)

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$\{A(k, i, j) : 0 \leq k < 100 \wedge 0 \leq i < \text{valueOf_m}(k) \wedge 0 \leq j < \text{valueOf_n}(k)\}$?

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\Rightarrow requires uninterpreted functions (of arity > 0)

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\Rightarrow requires uninterpreted functions (of arity > 0)

Alternative: use overapproximation of instance set and keep track of which elements are executed

Representing Dynamic Conditions

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      }

```

- **Instance set:** $\{A(k, i, j) : 0 \leq k < 100 \wedge 0 \leq i \wedge 0 \leq j\}$
- **Filter:**
 - ▶ **Filter access relations:** $\text{reader} \rightarrow (\text{writer} \rightarrow \text{array element})$
 - ★ $F_1^A = \{A(k, i, j) \rightarrow (M(k) \rightarrow m())\}$
 - ★ $F_2^A =$
 $\{A(0, i, j) \rightarrow (N1() \rightarrow n()); A(k, i, j) \rightarrow (N2(k-1) \rightarrow n()) : k \geq 1\}$
 - ▶ **Filter value relation:**
 $V^A = \{A(k, i, j) \rightarrow (m, n) : 0 \leq k \leq 99 \wedge 0 \leq i < m \wedge 0 \leq j < n\}$

Statement instance is executed iff values written by corresponding write accesses (through filter access relations) satisfy filter value relation

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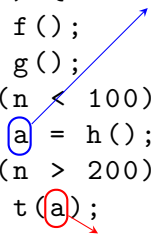
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Parametric Array Dataflow Analysis

```
while (1) { potential source
N:   n = f();
      a = g();
      if (n < 100)
H:   (a) = h();
      if (n > 200)
T:   t((a));
}
      sink
```



Is there any dataflow between potential source and sink at inner level?

Parametric Array Dataflow Analysis

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```

```

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    }
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sink

$$I = \{ H(i) : i \geq 0; T(i) : i \geq 0 \}$$

$$F^H = \{ H(i) \rightarrow (N(i) \rightarrow n()) \}$$

$$V^H = \{ H(i) \rightarrow (n) : i \geq 0 \wedge n < 100 \}$$

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- $F^H \circ M \subseteq F^T$

⇒ filter elements accessed by any potential source instance associated to sink instance forms subset of filter elements accessed by sink instance

Parametric Array Dataflow Analysis

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⇒ constraints on filter values at sink also apply at corresponding potential source: $V^T \circ M^{-1} = \{ H(i) \rightarrow (n) : i \geq 0 \wedge n > 200 \}$

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⇒ constraints on filter values at sink also apply at corresponding potential source: $V^T \circ M^{-1} = \{ H(i) \rightarrow (n) : i \geq 0 \wedge n > 200 \}$

- $(V^T \circ M^{-1}) \cap V^H = \emptyset$

⇒ there can be no dataflow at inner level

Polyhedral Process Networks

[19]

- Main purpose: extract task level parallelism from dataflow graph

statement → process
flow dependence → communication channel

⇒ requires dataflow analysis

- Processes are mapped to parallel hardware (e.g., FPGA)

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Example:

```
for (int i = 0; i < n; ++i) {  
S:      t = f1(A[i]);  
T:      B[i] = f2(t);  
}
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Example:

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for (int i = 0; i < n; ++i) {  
S:      t = f1(A[i]);  
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```

```
for (int i = 0; i < n; ++i)  
    write(fifo, f1(A[i]));
```

```
for (int i = 0; i < n; ++i)  
    B[i] = f2(read(fifo));
```

Process Networks with Dynamic Control

```
for (int i = 0; i < n; ++i) {  
S1:    t = f1(i);  
S2:    A[i] = t;  
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}
```

Run-time dependent dataflow:

$$\{ S1(i) \rightarrow S2(i); S3(i) \rightarrow S6(i) : \beta_{S6}^{S5} = 0; \\ S5(i) \rightarrow S6(i) : \beta_{S6}^{S5} = 1; S4(i) \rightarrow S5(i) \}$$

Process Networks with Dynamic Control

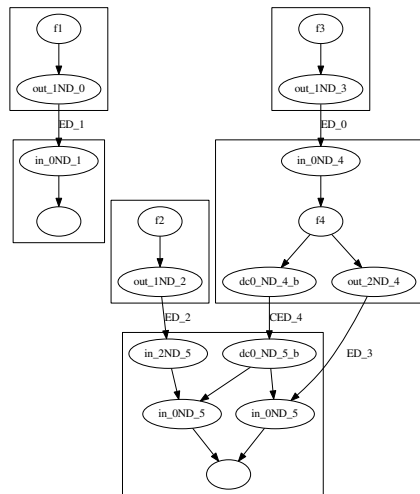
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Reductions

```
A:  s = 0;
    for (int i = 0; i < n; ++i)
B:      s += A[i];
C:  B[0] = s;
```

Dataflow:

$\{ A() \rightarrow B(0); B(i) \rightarrow B(i+1) : 0 \leq i < n-1; B(n-1) \rightarrow C() \}$

\Rightarrow fixes order of reduction

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Allow reordering of updates:

- read in C should depend on all updates in B
 - \Rightarrow updates should not kill dependences
 - \Rightarrow remove updates from must-writes
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Dataflow: $\{ A() \rightarrow B(i) : 0 \leq i < n; B(i) \rightarrow C() : 0 \leq i < n \}$

Outline

1 Polyhedral Model

- Introduction
- Representation

2 Polyhedral Transformation

- Schedules
- AST Generation

3 Dependences

- Schedule Validity
- Dependences
- Structures

4 Dataflow

- Parallelism
- Dataflow
- Approximate Dataflow
- Run-time dependent Dataflow
- Reductions

5 Aliasing

6 Counting

- Cardinality
- Bounds
- Weighted Counting
- Dynamic Memory Requirement

7 Transitive Closures

Aliasing

Some possible ways of handling aliasing:

- use an input language that does not permit aliasing
- pretend the problem does not exist
- require user to ensure absence of aliasing
⇒ e.g., use `restrict` keyword
- handle as may-write
⇒ may lead to too many dependences
- check aliasing at run-time
⇒ use original code in case of aliasing

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Cardinality

- Cardinality of a set

⇒ number of elements in the set

⇒ may depend on symbolic constants

$$S = \{ S(\mathbf{i}) : f(\mathbf{i}) \}$$

$$\text{card } S = \{ n : n = \# \mathbf{i} : f(\mathbf{i}) \}$$

$$\text{card} \left(\bigcup_i S_i \right) := \sum_i \text{card } S_i$$

$$\text{card} \{ A(i) : 0 \leq i \leq n; B() \} = n + 2$$

- Cardinality of a binary relation

⇒ for each domain element, number of corresponding images

$$R = \{ S(\mathbf{i}) \rightarrow T(\mathbf{j}) : f(\mathbf{i}, \mathbf{j}) \}$$

$$\text{card } R = \{ S(\mathbf{i}) \rightarrow n : n = \# \mathbf{j} : f(\mathbf{i}, \mathbf{j}) \} \quad \text{card} \left(\bigcup_i R_i \right) := \sum_i \text{card } R_i$$

$$R = \{ A(i) \rightarrow C(i) : 0 \leq i \leq n; B() \rightarrow C(i) : 0 \leq i \leq n \}$$

$$\text{card } R = \{ A(i) \rightarrow 1 : 0 \leq i \leq n; B() \rightarrow n + 1 \}$$

⇒ not a Presburger formula

Cardinality Examples

```
for (i = 0; i < N; ++i)
    for (j = 0; j < N - i; ++j)
        a[i+j] = f(a[i+j]);
```

- How many times is the statement executed?

$$\text{card}\{(i, j) : 0 \leq i < N \wedge 0 \leq j < N - i\}$$

$$\Rightarrow \left\{ \frac{N+N^2}{2} : N \geq 1 \right\}$$

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$$\text{card}(\{(i, j) \rightarrow a(i+j) : 0 \leq i < N \wedge 0 \leq j < N - i\})^{-1}$$

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$$\Rightarrow \{N : N \geq 1\}$$

Cardinality Examples (2)

How many times is S1 executed ?

```
for (i = max(0, N-M); i <= N-M+3; i++)
  for (j = 0; j <= N-2*i; j++)
    S1;
```

$$\text{card}\{(i, j) : 0, N - M \leq i \leq N - M + 3 \wedge 0 \leq j \leq N - 2i\}$$

$$\begin{cases} -4N + 8M - 8 & \text{if } M \leq N \leq 2M - 6 \\ MN - 2N - M^2 + 6M - 8 & \text{if } N \leq M \leq N + 3 \wedge N \leq 2M \\ \frac{N^2}{4} + \frac{3}{4}N + \frac{1}{2} \left\lfloor \frac{N}{2} \right\rfloor + 1 & \text{if } 0 \leq N \leq M \wedge 2M \leq N + 6 \\ \frac{N^2}{4} - MN - \frac{5}{4}N + M^2 + 2M + \frac{1}{2} \left\lfloor \frac{N}{2} \right\rfloor + 1 & \text{if } M \leq N \leq 2M \leq N + 6 \end{cases}$$

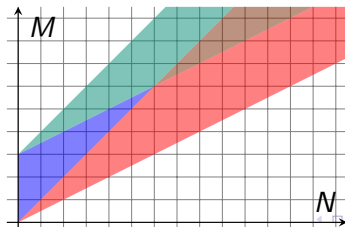
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Cardinality Representation

- Integer quasi affine expression

$$\lfloor x/2 \rfloor + 3N$$

⇒ Presburger term

That is, a term constructed from variables, symbolic constants, integer constants, addition (+), subtraction (−) and integer division by a constant ($\lfloor \cdot / d \rfloor$)

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$$x^2 - N/2$$

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- Piecewise quasi affine/polynomial expression

⇒ a list of pairs of pairs of Presburger sets and quasi affine/polynomial expressions $E = (S_i, e_i)_i$, with S_i disjoint

$$E(\mathbf{j}) = \begin{cases} e_i(\mathbf{j}) & \text{if } \mathbf{j} \in S_i \\ \perp/0 & \text{otherwise} \end{cases}$$

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Note: in practice, cardinality result does not contain nested integer divisions

Basic Operations on Piecewise Expressions

- Piecewise (rational) quasi affine expressions
 - ▶ addition ($+$)
 - ▶ subtraction ($-$)
 - ▶ negation ($-$)
 - ▶ minimum (\min), maximum (\max)
 - ▶ multiplication by constant ($\cdot d$)
 - ▶ division by constant ($/d$)
 - ▶ remainder on integer division by constant ($\bmod d$)
 - ▶ floor ($\lfloor \cdot \rfloor$)
 - ▶ ceiling ($\lceil \cdot \rceil$)

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 - ▶ exponentiation by positive integer constant (\cdot^d)

Bounds on Piecewise Quasi Polynomials

$$m(N) = \max_{(x,y): x,y \geq 0 \wedge x+y \leq N} 4+x+y-(x-2)^2$$

Question

Can exact maximum be computed in general?

Bounds on Piecewise Quasi Polynomials

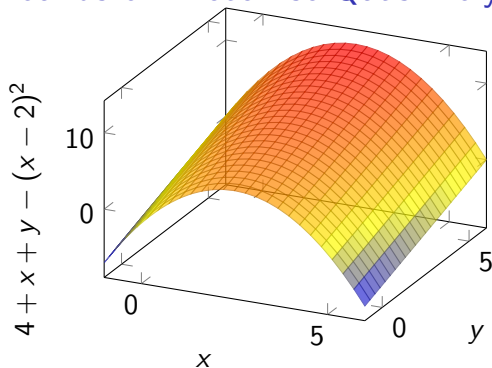
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Upper bound $u(N) \geq m(N)$ can be computed

Bounds on Piecewise Quasi Polynomials



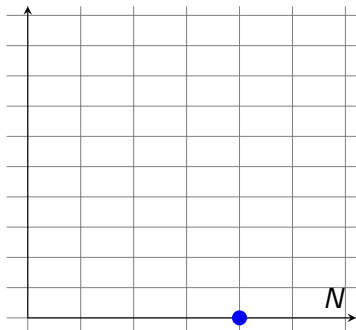
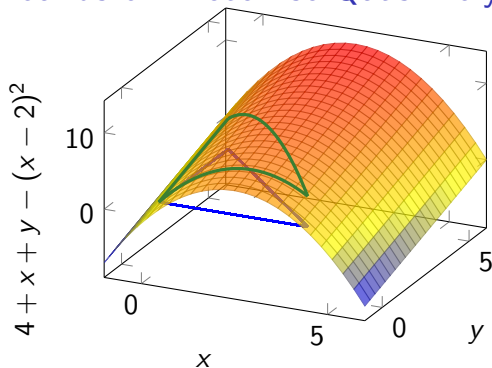
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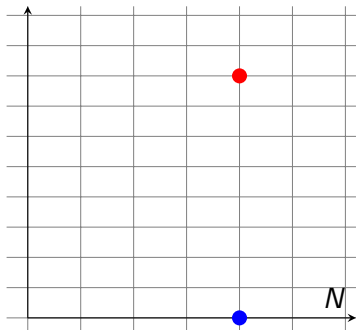
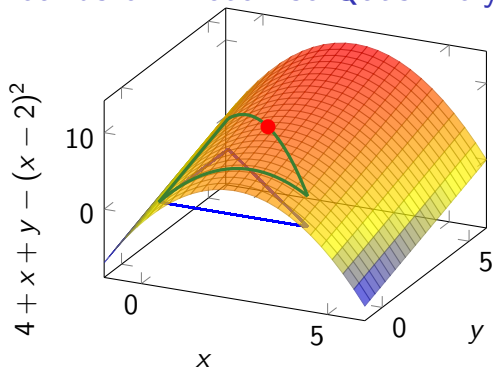
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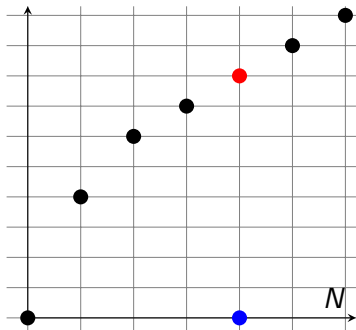
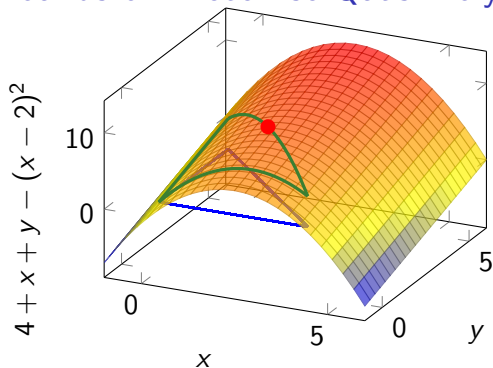
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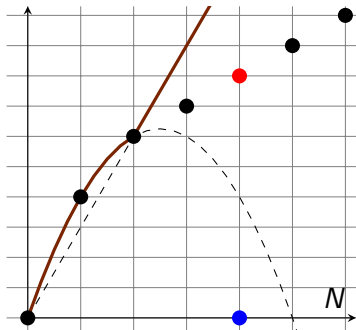
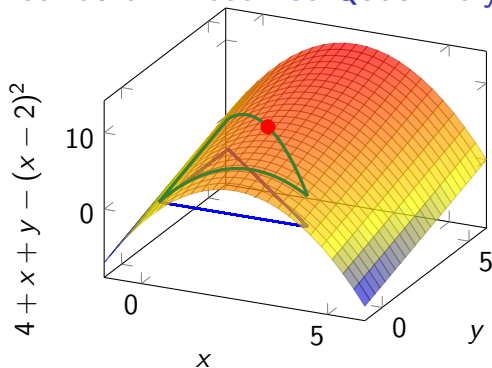
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Upper bound $u(N) \geq m(N)$ can be computed

Bounds on Piecewise Quasi Polynomials



$$m(N) = \max_{(x,y): x,y \geq 0 \wedge x+y \leq N} 4 + x + y - (x - 2)^2 \leq u(N) = \max(3N, 5N - N^2)$$

Question

Can exact maximum be computed in general?

Upper bound $u(N) \geq m(N)$ can be computed

Bounds on Piecewise Quasi Polynomials — Example

```
for (i = 0; i < N; ++i)
  for (j = i; j < N; ++j) {
    p = malloc(i * j + i - N + 1);
    /* ... */
    free(p);
  }
```

How much memory is needed?

Bounds on Piecewise Quasi Polynomials — Example

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$$\text{ub} \left\{ ij + i - N + 1 \quad \text{if } 0 \leq i < N \wedge i \leq j < N \right.$$

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  }
```

How much memory is needed?

$$\text{ub} \left\{ ij + i - N + 1 \quad \text{if } 0 \leq i < N \wedge i \leq j < N \right.$$

Result:

$$\left\{ \max(1 - 2N + N^2) \quad \text{if } N \geq 1 \right.$$

(exact maximum)

Maximal Number of Live Memory elements

- Assume each statement instance writes to at most one array element
 \Rightarrow Each live element can be identified by write instance

- Compute dataflow relation D

- For each write instance compute last read

$$L = O^{-1} \circ \text{lexmax}(O \circ D)$$

- For each statement instance i , count **write** instances that **precede** i such that **corresponding last read follows** i

\Rightarrow Number of live elements at i

$$N = \text{card} \left((((O \succ O) \cap_{\text{ran}} (\text{dom } L)) \cap (L^{-1} \circ (O \preccurlyeq O))) \right)$$

- Compute upper bound

$$U = \text{ub } N$$

Maximal Number of Live Memory elements — Example

```
for (i = 0; i < N; ++i)
```

```
S1:      t[i] = f(a[i]);
```

```
for (i = 0; i < N; ++i)
```

```
S2:      b[i] = g(t[N-i-1]);
```

$$I = \{S1(i) : 0 \leq i < N; S2(i) : 0 \leq i < N\}$$

$$O = \{S1(i) \rightarrow (0, i); S2(i) \rightarrow (1, i)\}$$

$$D = \{S1(i) \rightarrow S2(N-1-i) : 0 \leq i < N\}$$

Maximal Number of Live Memory elements — Example

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$$L = O^{-1} \circ \text{lexmax}(O \circ D)$$

$$= \{S1(i) \rightarrow S2(N-1-i) : 0 \leq i < N\}$$

$$N = \text{card} \left((((O \succ O) \cap_{\text{ran}} (\text{dom } L)) \cap (L^{-1} \circ (O \preccurlyeq O))) \right)$$

$$= \{S1(i) \rightarrow i : 1 \leq i < N; S2(i) \rightarrow N-i : 0 \leq i < N\}$$

$$U = \text{ub } N$$

$$= \{\max(N) : N \geq 1\}$$

Weighted Counting

$$G = F \circ R$$

with F a piecewise quasi polynomial and R a Presburger relation is a piecewise quasi polynomial G such that

$$G(\mathbf{i}) = \sum_{\mathbf{j}: R(\mathbf{i}, \mathbf{j})} F(\mathbf{j})$$

Weighted Counting

$$G = F \circ R = \left\{ (x, y) \rightarrow \frac{x^2 + y^2}{4} : 1 \leq x, y \leq 2 \right\} \circ \{ (x) \rightarrow (x, y) \}$$

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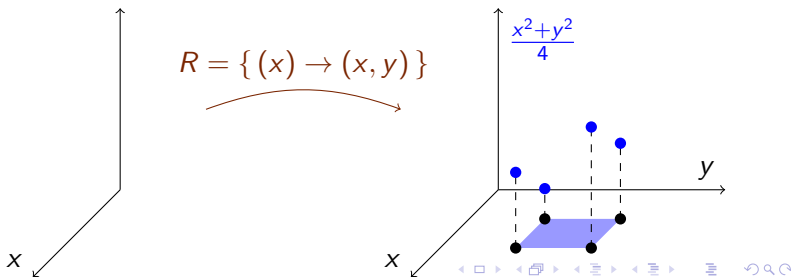
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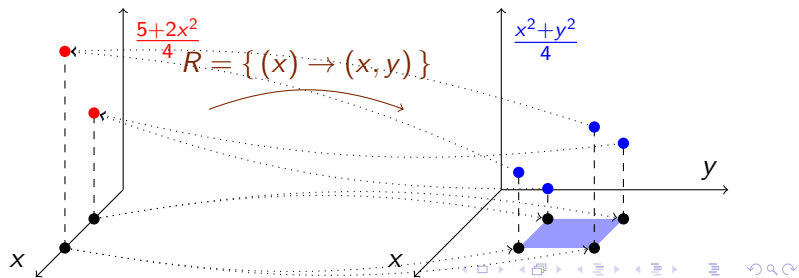
Weighted Counting

$$G = F \circ R = \left\{ (x, y) \rightarrow \frac{x^2 + y^2}{4} : 1 \leq x, y \leq 2 \right\} \circ \{ (x) \rightarrow (x, y) \}$$

$$= \left\{ (x) \rightarrow \frac{5 + 2x^2}{2} : 1 \leq x \leq 2 \right\}$$

with F a piecewise quasi polynomial and R a Presburger relation is a piecewise quasi polynomial G such that

$$G(i) = \sum_{j: R(i, j)} F(j)$$



Compositions with Piecewise (Folds of) Quasi polynomials

$$F \circ R$$

- $R: D_1 \rightarrow D_2$ is a Presburger relation
- $F: D_2 \rightarrow \mathbb{Q}$ may be
 - ▶ piecewise quasi polynomial
(result of counting problem)
 - \Rightarrow take sum over $(\text{ran } R) \cap (\text{dom } F)$
 - ▶ piecewise fold of quasi polynomials
(result of upper bound computation)
 - \Rightarrow compute bound over $(\text{ran } R) \cap (\text{dom } F)$
- $(F \circ R): D_1 \rightarrow \mathbb{Q}$ of same type as F

if R is single-valued, then sum/bound is computed over a single point

Compositions with Piecewise (Folds of) Quasi polynomials

$$F \circ R \quad \text{or} \quad F(S)$$

- $R: D_1 \rightarrow D_2$ is a Presburger relation
- $S \subseteq D_2$ is a Presburger set
- $F: D_2 \rightarrow \mathbb{Q}$ may be

- ▶ piecewise quasi polynomial
(result of counting problem)

\Rightarrow take sum over $(\text{ran } R) \cap (\text{dom } F)$ or $S \cap (\text{dom } F)$

- ▶ piecewise fold of quasi polynomials
(result of upper bound computation)

\Rightarrow compute bound over $(\text{ran } R) \cap (\text{dom } F)$ or $S \cap (\text{dom } F)$

- $(F \circ R): D_1 \rightarrow \mathbb{Q}$ of same type as F
- $F(S): \mathbb{Q}$ of same type as F

if R is single-valued, then sum/bound is computed over a single point

Example: Total Memory Allocation

```
for (i = 0; i < N; ++i)
    for (j = i; j < N; ++j)
        p[i][j] = malloc(i * j + i - N + 1);
/* ... */
for (i = 0; i < N; ++i)
    for (j = i; j < N; ++j)
        free(p[i][j]);
```

How much memory allocated in total?

Example: Total Memory Allocation

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for (i = 0; i < N; ++i)
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        p[i][j] = malloc(i * j + i - N + 1);
/* ... */
for (i = 0; i < N; ++i)
    for (j = i; j < N; ++j)
        free(p[i][j]);
```

How much memory allocated in total?

$$F = \{ (i, j) \rightarrow ij + i - N + 1 \}$$

$$I = \{ (i, j) : 0 \leq i < N \wedge i \leq j < N \}$$

$$F(I) = \left\{ \frac{5}{12}N - \frac{1}{8}N^2 - \frac{5}{12}N^3 + \frac{1}{8}N^4 : N \geq 1 \right\}$$

Dynamic Memory Requirement Estimation

How much memory is needed to execute the following program?

```
void m0(int m) {  
    for (c = 0; c < m; c++) {  
        m1(c);                /*S1*/  
        B[] m2Arr = m2(2*m-c); /*S2*/  
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void m1(int k) {  
    for (i = 1; i <= k; i++) {  
        A a = new A();        /*S3*/  
        B[] dummyArr = m2(i); /*S4*/  
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}  
  
B[] m2(int n) {  
    B[] arrB = new B[n];      /*S5*/  
    for (j = 1; j <= n; j++)  
        B b = new B();        /*S6*/  
    return arrB;  
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```

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Dynamic Memory Requirement Estimation

How much (scoped) memory is needed?

⇒ compute for each method

ret_m size of memory returned by m

cap_m size of memory “captured” (not returned) by m

$memRq_m$ total memory requirements of m

$$ret_m + cap_m = \sum_{p \text{ called by } m} ret_p$$

$$memRq_m = cap_m + \max_{p \text{ called by } m} memRq_p$$

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$$\text{memRq}_m = \text{cap}_m + \max_{p \text{ called by } m} \text{memRq}_p$$

⇒ summarize over iteration domain, i.e., compose with $M = (\underline{\text{dom}} \rightarrow)^{-1}$

$$M = \{ m0(m) \rightarrow (m0(m) \rightarrow S1(c)) : 0 \leq c < m; \\$$

$$m0(m) \rightarrow (m0(m) \rightarrow S2(c)) : 0 \leq c < m; \\$$

$$m1(k) \rightarrow (m1(k) \rightarrow S3(i)) : 1 \leq i \leq k; \\$$

$$m1(k) \rightarrow (m1(k) \rightarrow S4(i)) : 1 \leq i \leq k; \\$$

$$m2(n) \rightarrow (m2(n) \rightarrow S5()); m2(n) \rightarrow (m2(n) \rightarrow S6(j)) : 1 \leq j \leq n \}$$

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```

$$\text{ret}_{m2} = \{ (m2(n) \rightarrow S5()) \rightarrow n : n \geq 0 \} \circ M$$

$$\text{cap}_{m2} = \{ (m2(n) \rightarrow S6(j)) \rightarrow 1 \} \circ M$$

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Dynamic Memory Requirement Estimation

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```
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```

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ret_{m2} is a function of the arguments of $m2$

We want to use it as a function of the arguments and local variables of $m1$

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    B[] arrB = new B[n];      /*S5*/  
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$$B^{m0} = \{ (m0(m) \rightarrow S1(c)) \rightarrow m1(c); \\ (m0(m) \rightarrow S2(c)) \rightarrow m2(2m - c) \}$$

$$B^{m1} = \{ (m1(k) \rightarrow S4(i)) \rightarrow m2(i) \}$$

Dynamic Memory Requirement Estimation

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$$\text{cap}_{\text{m1}}(k) = \sum_{1 \leq i \leq k} (1 + \text{ret}_{\text{m2}}(i))$$

$$\text{ret}_{\text{m1}} = \{ \text{m1}(k) \rightarrow 0 \}$$

$$\text{cap}_{\text{m1}} = (\{ (\text{m1}(k) \rightarrow \text{S3}(i)) \rightarrow 1 \} + \text{ret}_{\text{m2}} \circ B^{\text{m1}}) \circ M$$

$$\text{memRq}_{\text{m1}} = \text{cap}_{\text{m1}} + (\text{memRq}_{\text{m2}} \circ B^{\text{m1}} \circ M)$$

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Dynamic Memory Requirement Estimation

```

void m0(int m) {
    for (c = 0; c < m; c++) {
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```

$$B^{m_0} = \{ (m_0(k) \rightarrow S1(c)) \rightarrow m1(c); (m_0(k) \rightarrow S2(c)) \rightarrow m2(2m - c) \}$$

$$\text{ret}_{m_0} = \{ m_0(m) \rightarrow 0 \}$$

$$\text{cap}_{m_0} = (\text{ret}_{m_1} + \text{ret}_{m_2}) \circ B^{m_0} \circ M$$

$$\text{memRq}_{m_0} = \text{cap}_{m_0} + ((\text{memRq}_{m_1} + \text{memRq}_{m_2}) \circ B^{m_0} \circ M)$$

Dynamic Memory Requirement Estimation

```

void m0(int m) {
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$$ret_{m0} = \{ m0(m) \rightarrow 0 \}$$

$$cap_{m0} = (ret_{m1} + ret_{m2}) \circ B^{m0} \circ M$$

$$= \left\{ m0(m) \rightarrow \frac{1}{2}m + \frac{3}{2}m^2 : m \geq 1 \right\}$$

$$memRq_{m0} = cap_{m0} + ((memRq_{m1} + memRq_{m2}) \circ B^{m0} \circ M)$$

$$= \left\{ m0(m) \rightarrow \max \left(-2 + 2m + 2m^2, \frac{5}{2}m + \frac{3}{2}m^2 \right) : m \geq 1 \right\}$$

Outline

1 Polyhedral Model

- Introduction
- Representation

2 Polyhedral Transformation

- Schedules
- AST Generation

3 Dependences

- Schedule Validity
- Dependences
- Structures

4 Dataflow

- Parallelism
- Dataflow
- Approximate Dataflow
- Run-time dependent Dataflow
- Reductions

5 Aliasing

6 Counting

- Cardinality
- Bounds
- Weighted Counting
- Dynamic Memory Requirement

7 Transitive Closures

Positive Powers

Definition (Power of a Relation)

Let R be a Presburger relation and k a positive integer, then power k of relation R is defined as

$$R^k := \begin{cases} R & \text{if } k = 1 \\ R \circ R^{k-1} & \text{if } k \geq 2. \end{cases}$$

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Example

$$R = \{ (x) \rightarrow (x + 1) \}$$

$$R^k = \{ (x) \rightarrow (x + k) : k \geq 1 \}$$

Transitive Closures

Definition (Transitive Closure of a Relation)

Let R be a Presburger relation, then the transitive closure R^+ of R is the union of all positive powers of R ,

$$R^+ := \bigcup_{k \geq 1} R^k.$$

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$$R^+ = \{ (x) \rightarrow (y) : \exists k \geq 1 : y = x + k \} = \{ (x) \rightarrow (y) : y \geq x + 1 \}$$

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Definition (Transitive Closure of a Relation, Alternative)

Inductive definition:

$$R^+ := R \cup (R \circ R^+)$$

Transitive Closures — Approximation

Fact

Given a Presburger relation R , the power R^k (with k a parameter) and the transitive closure R^+ may not be Presburger relations.

Example

$$R = \{ (x) \rightarrow (2x) \}$$
$$R^k = \{ (x) \rightarrow (2^k x) \}$$

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Given a Presburger relation R , the power R^k (with k a parameter) and the transitive closure R^+ may not be Presburger relations.

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⇒ need for approximation

- ▶ overapproximation R^+
- ▶ underapproximation R^\pm

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Given a Presburger relation R , the power R^k (with k a parameter) and the transitive closure R^+ may not be Presburger relations.

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⇒ need for approximation

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Note

Do not use transitive closures if there is an alternative.

Part II

Tools

Outline

8 Tools

Availability — Representation

[20]

$$\{ A(i) : 0 \leq i \leq n; B(i,j) : \exists \alpha : i = 2\alpha \}$$

- Named (and nested) spaces: `isl`

```
[n] -> { A[i]: 0 <= i <= n; B[i,j]: exists a: i = 2 a }
```

In `omega(+)`:

Availability — Representation

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$$\{ A(i) : 0 \leq i \leq n; B(i,j) : \exists \alpha : i = 2\alpha \}$$

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```

In omega(+):

```
symbolic n;
```

```
{ [0, i, 0]: 0 <= i <= n } union { [1, i,j]: exists a: i = 2 a }
```

A

padding

B

Availability — Representation

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A padding

B

- Presburger sets and relations: `isl`, `omega(+)`

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symbolic n;

{ [0, i, 0]: 0 <= i <= n } union { [1, i,j]: exists a: i = 2 a }

A padding

B

- Presburger sets and relations: isl, omega(+)

In PolyLib:

2 equality/inequality n

4 6

0	-1	0	0	0	0
0	0	0	-1	0	0
1	0	1	0	0	0
1	0	-1	0	1	0

2 7

0	-1	0	0	0	0	-1
0	0	-1	0	2	0	0

Moreover: PolyLib deals with rational sets (polyhedra)

Availability — Representation (2)

- Uninterpreted functions: $\text{omega}(+)$

- ⇒ arity can be greater than 0

- ⇒ not available in `isl` (yet)

Note: support in $\text{omega}(+)$ for uninterpreted functions is very restrictive

- ▶ arguments need to be prefix of input/output dimensions
 - ⇒ essentially symbolic constants

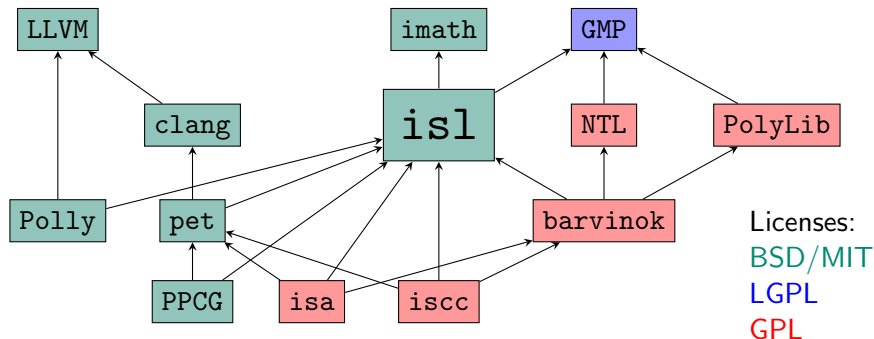
Availability — Operations

	union	intersection	difference	emptiness	domain range inverse application
PolyLib	✓	✓	in \mathbb{Z}		
PPL	✓	✓		✓	
PIP				lexmin	
omega(+)	✓	✓	✓	✓	✓
isl	✓	✓	✓	✓	✓
barvinok				card	
bernstein					
LattE				card	

Availability — Operations (2)

	lexmin	cardinality	bounds on polynomials	weighted counting	transitive closure
PolyLib		✓			
PPL	✓				
PIP	✓				
omega(+)	Presburger				R^{\pm}
isl	✓		✓		R^{\mp}
barvinok	partial	✓		✓	
bernstein			✓		
LattE		✓		✓	

isl and Related Libraries and Tools



isl: manipulates parametric affine sets and relations

barvinok: counts elements in parametric affine sets and relations

pet: extracts polyhedral model from clang AST

PPCG: Polyhedral Parallel Code Generator

iscc: interactive calculator

isa: prototype tool set including derivation of process networks and equivalence checker

Overview of isl

isl is a thread-safe C library for manipulating **integer sets and relations**

- bounded by *affine constraints*
- involving *symbolic constants* and
- *existentially quantified variables*

and **quasi-affine** and **quasi-polynomial functions** on such domains

Supported operations by core library include

- *intersection*
- *union*
- *set difference*
- *integer projection*
- *coalescing*
- *closed convex hull*
- *sampling, scanning*
- *integer affine hull*
- *lexicographic optimization*
- *transitive closure* (approx.)
- *parametric vertex enumeration*
- *bounds on quasipolynomials*

Polyhedral compilation library

- *schedule trees*
- *scheduling*
- *dataflow analysis*
- *AST generation*

PPCG

PPCG (<http://ppcg.gforge.inria.fr/>)

- Input: C code
- Output: CUDA or OpenCL code for GPGPUs

PPCG

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- Input: C code
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Steps:

- extract polyhedral model from C code (pet)
- dependence analysis (isl)
- scheduling
 - ▶ expose parallelism and tiling opportunities (isl)
 - ▶ perform tiling (isl)
 - ▶ separate into parts mapped to host, GPU blocks and GPU threads
- memory management
 - ▶ add transfers of data to/from GPU
 - ▶ detect array reference groups
 - ▶ allocate groups to registers and shared memory
- generate AST (isl)

PPCG Example — Input

```
void matmul(int M, int N, int K,  
            float A[static const restrict M][K],  
            float B[static const restrict K][N],  
            float C[static const restrict M][N])  
{  
    for (int i = 0; i < M; i++)  
        for (int j = 0; j < N; j++) {  
S1:    C[i][j] = 0;  
        for (int k = 0; k < K; k++)  
S2:    C[i][j] = C[i][j] + A[i][k] * B[k][j];  
        }  
}
```

Options:

```
--ctx="[M,N,K] -> { : M = N = K = 256 }"  
--sizes="{ kernel[i] -> tile[16,16,16];  
           kernel[i] -> block[8,16] }"  
--pet-autodetect
```


PPCG Example — Output

```
long b0 = blockIdx.y, b1 = blockIdx.x;
long t0 = threadIdx.y, t1 = threadIdx.x;
__shared__ float s_A[16][16];
float p_C[2][1];
__shared__ float s_B[16][16];

for (long g9 = 0; g9 <= 15; g9 += 1) {
    for (long c0 = t0; c0 <= 15; c0 += 8)
        s_B[c0][t1] = B[(16 * g9 + c0) * (256) + 16 * b1 + t1];
    for (long c0 = t0; c0 <= 15; c0 += 8)
        s_A[c0][t1] = A[(16 * b0 + c0) * (256) + t1 + 16 * g9];
    __syncthreads();
    if (g9 == 0) {
        p_C[0][0] = (0);
        p_C[1][0] = (0);
    }
    for (long c3 = 0; c3 <= 15; c3 += 1) {
        p_C[0][0] = (p_C[0][0] + (s_A[t0][c3] * s_B[c3][t1]));
        p_C[1][0] = (p_C[1][0] + (s_A[t0 + 8][c3] * s_B[c3][t1]));
    }
    __syncthreads();
}

C[(16 * b0 + t0) * (256) + 16 * b1 + t1] = p_C[0][0];
C[(16 * b0 + t0 + 8) * (256) + 16 * b1 + t1] = p_C[1][0];
```

CARP Project

Design tools and techniques to aid **Correct and Efficient Accelerator Programming**

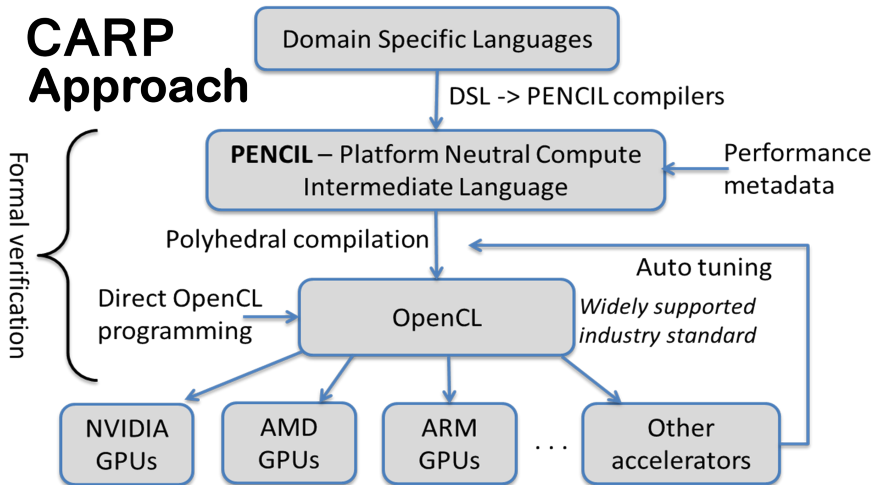
Key areas:

- High level programming models
- Advanced compilation techniques
- Formal verification

Partners:

- Imperial College London (UK)
- ENS (FR)
- ARM (UK)
- Realeyes (ES)
- RWTH Aachen University (DE)
- Monoidics (UK)
- University of Twente (NL)
- Rightware (FI)

CARP Approach



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